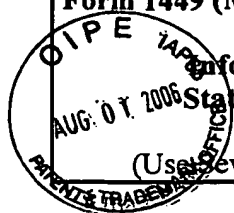


**Form 1449 (Modified)****Information Disclosure  
Statement By Applicant**

(Use Several Sheets if Necessary)

Atty Docket No.  
ALTRP189/A1043  
Applicant:  
Schleicher et al.  
Filing Date  
March 25, 2004Application No.:  
10/810,117  
Group  
2819**U.S. Patent Documents**

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
JC	A1.	6,798,240	09/28/04	Lewis et al.			
JC	A2.	6,943,580	09/13/05	Lewis et al.			
	A3.						
	A4.						
	A5.						

**Foreign Patent or Published Foreign Patent Application**

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	B1.							
	B2.							
	B3.							
	B4.							
	B5.							

**Other Documents**

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
JC	C1.	U.S. Patent Application No. 10/693,576 "Arithmetic Structure is for Programmable Logic Device" filed October 23, 2003
JC	C2.	U.S. Patent Application No. 10/718,968 "Logic Cell Supporting Addition of Three Binary Words" filed November 21, 2003
	C3.	
	C4.	
	C5.	

Examiner	/James Cho/	Date Considered	08/10/2006
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Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.